



Docket No.: 03226/163001; P7058
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Claude R. Gauthier et al.

Application No.: 10/075,783

Confirmation No.: 9162

Filed: February 14, 2002

Art Unit: 2128

For: METHOD FOR OPTIMIZING
DECOUPLING CAPACITOR DESIGN IN
DELAY LOCKED LOOPS

Examiner: F. O. Ferris

INFORMATION DISCLOSURE STATEMENT (IDS)

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed after the mailing date of a Final Office Action or Notice of Allowance, whichever occurred first, but on or before payment of the Issue Fee (37 CFR 1.97(d)). Applicant(s) hereby petition(s) that the Information Disclosure Statement be considered.

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I hereby certify, pursuant to 37 CFR 1.97(e)(2), that no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Applicant submits herewith copies of foreign and non-patents in accordance with 37 CFR 1.98(a)(2).

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0591, under Order No. 03226/163001; P7058.

Dated: May 2, 2006

Respectfully submitted,

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PTO/SB/08a/b (07-05)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Application Number	10/075,783-Conf. #9162
				Filing Date	February 14, 2002
				First Named Inventor	Claude R. Gauthier
				Art Unit	2128
				Examiner Name	F. O. Ferris
Sheet	1	of	1	Attorney Docket Number	03226/163001; P7058

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	HEYDARI, et al; "Analysis of Jitter due to Power-Supply Noise in Phase-Locked Loops"; Dept. of EE Systems, Univ. of Southern California; 2000 IEEE Integrated Circuits Conference; pp. 443-446 (4 pages)	
	CB	EGAN, et al.; "Characterization and Verification of Phase-Locked Loops"; IEEE Instrumentation and Measurement Technology Conference, Budapest, Hungary, May 21-23, 2001, pp. 1697-1702 (6 pages)	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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